


A²
5. (Amended) The method for fabricating semiconductor devices according to claim 3, wherein said etching process is performed by setting the processing plasma pressure to below 0.5Pa.

Sub B27
6. (Amended) The method for fabricating semiconductor devices according to claim 3, further comprising, immediately after said etching process, a process for subjecting to said sample an O₂ or H₂ plasma processing.

REMARKS

Examination is requested.

Respectfully submitted,


John R. Mattingly
Registration No. 30,293
Attorney for Applicants

MATTINGLY, STANGER & MALUR
1800 Diagonal Road, Suite 370
Alexandria, Virginia 22301
(703) 684-1120
Date: August 3, 2001

MARKED UP VERSION OF REWRITTEN CLAIMS

3. (Amended) The method for fabricating semiconductor devices according to claim 1 [or 2], further comprising, after said third step and before said fourth step, an etching process for removing predetermined part of said first insulating material layer by etching by means of plasma of a mixed gas containing NF_3 and Ar through an opening patterned in said second insulating material layer in said third step.

5. (Amended) The method for fabricating semiconductor devices according to claim[s] 3 [or 4], wherein said etching process is performed by setting the processing plasma pressure to below 0.5Pa.

6. (Amended) The method for fabricating semiconductor devices according to [any one of] claim[s] 3 [to 5], further comprising, immediately after said etching process, a process for subjecting to said sample an O_2 or H_2 plasma processing.